

Fig.1.(a) Proposed single-phase TPC. (b) Conventional h-bridge and buck boost for dc/dc/ac system.

Three-port converters (TPC) offer increased power density, fewer components, and improved efficiency [10]. TPC topologies focus on soft-switched converters or modified PWM.

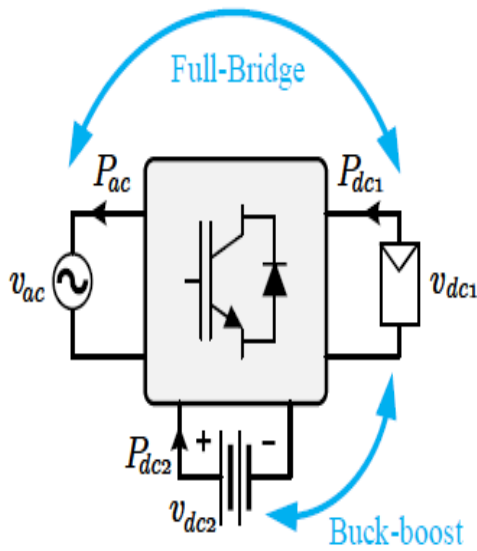


Fig. 2. Proposed TPC for a PV-BESS application.

In soft-switched solutions, the first stage contains three dc ports and the second stage incorporates a dc/ac converter [9], [11],–[13]. The second step to link the ac system affects the first stage's (dc/dc conversion) efficiency. Modified PWM TPC systems use active and passive components and offer superior control and modulation techniques [14]–[18]. The second TPC technique reduces power losses, component count, and failure rate. This study proposes a bidirectional TPC to connect two dc ports with single-phase or three-phase ac. Each converter port can link power sources or loads. Each inverter leg is a buck-boost converter, simplifying the architecture. Fig. 1 shows how this buck-boost converter employs the same number of semiconductors as typical inverters and adds one inductor per leg. FCS-MPC regulates power flow across ports using MIMO state-space modeling. Simulations and experiments show the recommended converter's bidirectional performance and boost ability, allowing low dc voltage to be linked to the ac grid without a transformer. The

proposed approach only comprises one power conversion stage between dc and ac ports, unlike earlier three-port converters. Previous TPC topologies required more stages or high-power transistors to do the same tasks. The suggested solution improves power density and flexibility because all ports are bidirectional. This is possible thanks to a proposed control technique that governs power transfer between the three ports by performing a coupled computation of two controlled variables (v_a ; v_b).

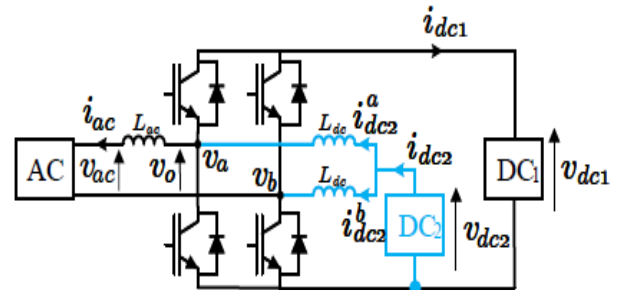


Fig. 3. (a) Proposed single-phase TPC

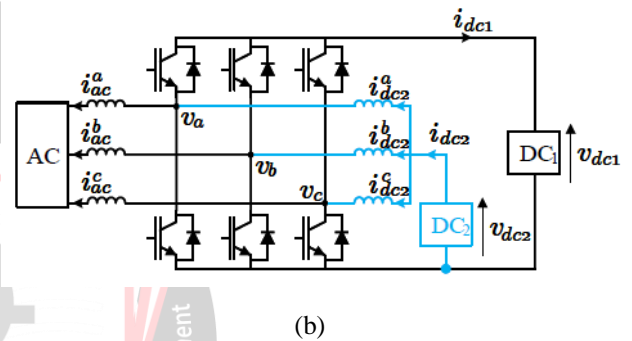


Fig. 3. (b) Proposed three-phase TPC

II. EXISTING METHOD OF THREE PORT CONVERTER (TPC)

The Existing method of TPC is based on a full-bridge converter with each leg acting as a buck-boost converter at the same time. As a result, the ac port and the dc1 port interact similarly to a normal full-bridge, whereas the dc2 port and the dc1 port interact similarly to a conventional buck-boost converter (see Fig. 2). The architecture is straightforward, but the simultaneous operation as a full-bridge and buckboost converter necessitates multi-variable control in order to accomplish three-port governance. As demonstrated in Fig. 3, the TPC can be used in single-phase or three-phase designs. The dc2 port is decoupled from the ac side and can operate at a lower voltage than the dc1 port, which has a minimum value set by the peak ac voltage. PV modules, batteries, capacitors, fuel cells, and dc micro-grids are examples of different sorts of sources or loads that could be represented by both dc ports. The ac systems could represent the grid, an ac motor or load, or an ac generator, among other things. The type of sources/loads, on the other hand, will set constraints and

specific control goals for the system's voltages and currents[4].

A. Operation Principle:

A new degree of freedom is required in order to control the power at the new third port, as conventional linear control schemes just regulate one variable between two ports. Thus, instead of using only v_o as manipulated variable, the required controller should regulate both v_a and v_b separately to control the currents at all ports.

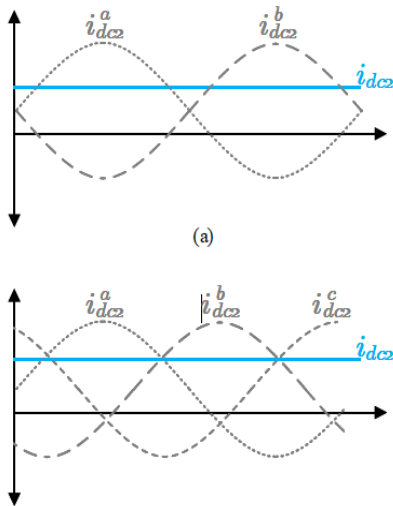


Fig. 4. Current waveforms of the second dc port inductors. (a) Single-phase TPC. (b) Three-phase TPC.

The change from one to two manipulated variables provides the capability to control i_{ac} and i_{dc2} at the same time. The dynamical equations (1)-(2) describe the behaviour of the regulated variables for the single-phase case.

$$\frac{di_{ac}}{dt} = \frac{1}{L_{ac}} \left(\overbrace{(v_a - v_b)}^{v_o} - r_{ac} \cdot i_{ac} - v_{ac} \right) \quad (1)$$

$$\frac{di_{dc2}}{dt} = \frac{1}{L_{dc}} (2v_{dc2} - (v_a + v_b) - r_{dc} \cdot i_{dc2}) \quad (2)$$

The current i_{dc2} is generated by the sum of the currents of the interface inductors L_{dc} . These currents contain both dc and ac components defined by the voltage v_{dc2} and the voltages v_a and v_b from the converter legs. The oscillating component at the frequency of the ac port is canceled at the dc2 port by using 180_ phase-shift between the inductor currents, as it is shown in Fig. 4(a).

B. Voltage and Power Characteristics

The design of the converter (component sizing and control scheme) depends on the systems to be connected at each port, as they could require voltage or current control and unidirectional or bidirectional power flow[6]. Therefore, in order to characterize the topology, with a capacitor at $dc1$, a battery at $dc2$ and the single-phase grid at the ac side. This configuration allows to: connect a

battery to the grid with a small capacitance at the dc-bus, as the battery is placed at the second dc-port which is free of low-frequency harmonics; and reduce the required series connected battery cells, as v_{dc2} can be lower than the peak voltage at the ac port. Furthermore, an additional source as a PV can be connected in parallel with the capacitor at $dc1$ to use the converter in a hybrid PV-BESS application. Thus, the following analysis applies to grid-connected BESS with boost capability and hybrid PV-BESS also connected to the ac-grid.

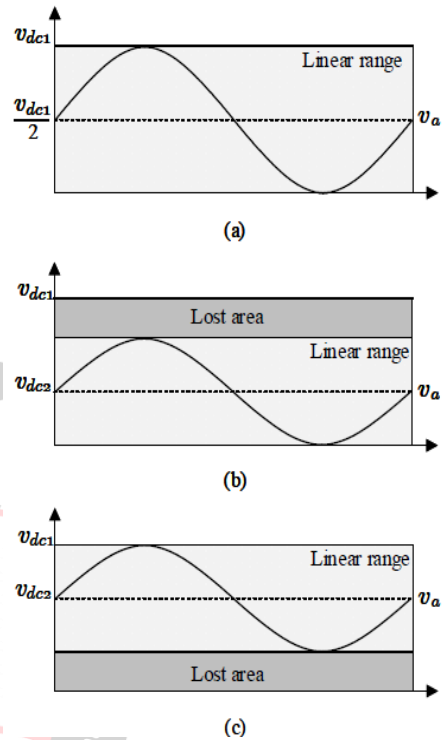


Fig. 5. Operating range for leg voltages. (a) Regular h-bridge. (b) Proposed TPC with $v_{dc2} < 0.5 v_{dc1}$. (c) Proposed TPC with $v_{dc2} > 0.5 v_{dc1}$

In the proposed TPC, voltage ratio between v_{dc2} and v_{dc1} defines the dc component of the voltages v_a and v_b generated by the legs of the converter. This bias is highly relevant as it determines the maximum peak voltage of v_o . In a regular h-bridge, the dc component for each leg is always $0.5v_{dc1}$, thus each voltage could use the entire linear range to generate the sinusoidal component. However, in the TPC topology, variations on the v_{dc2} voltage reduce the useful linear range and hence limit the maximum output voltage[11], as it is shown in Fig. 5. Consequently, in order to use the full voltage range at the ac output, the relation between both dc voltages should be $v_{dc2} = 0.5v_{dc1}$. However, different voltage ratios and rated power can be selected according to the application. Considering the mentioned applications with a battery at $dc2$, the variations on the voltage depending on the state of charge should be included in the design to determine the limits of the operating range. The active power at the ac port is characterized by (3), where V_o and V_{ac} represent

the magnitudes of the output and grid voltages respectively and δ is the phase angle between both voltages. The power equation is the same of a regular dc/ac converter, but the exposed limitation on the output voltage should be considered according to (4). Therefore, the power characteristic depends on the relation of the dc voltages.

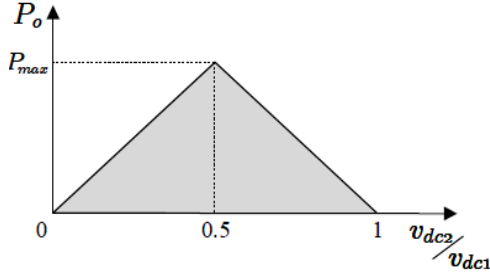


Fig. 6. Output power range vs. vdc2=vdc1 ratio

and it is shown in Fig. 6, where δ is determined by the power factor delivered.

$$P_o = \frac{V_o \cdot V_{ac}}{X_{Lac}} \sin(\delta) \tag{3}$$

$$V_o^{max} = \begin{cases} 2 \cdot v_{dc2} & v_{dc2} \leq 0.5 \cdot v_{dc1} \\ 2 \cdot (v_{dc1} - v_{dc2}) & v_{dc2} > 0.5 \cdot v_{dc1} \end{cases} \tag{4}$$

III. CONTROL METHOD OF THE TPC

The proposed configuration in Fig. 3(a) presents three variables to be controlled (v_o , i_{dc2} , v_{dc1}) and two manipulated variables (v_a , v_b). Thus, a multiple-input and multiple-output (MIMO) state-space approach was chosen to model and control the dynamics of the converter. Each manipulated voltage is determined by the switching state of the respective leg and the dc bus voltage as $v_i = S_i \cdot v_{dc1}$ with $i \in \{a, b\}$, $s_i \in \{1, 0\}$. Consequently, the proposed model (5) presents nonlinear terms given by the product of control inputs with state variables[7].

$$\frac{d}{dt} \begin{bmatrix} i_{ac} \\ i_{dc2} \\ v_{dc1} \end{bmatrix} = \begin{bmatrix} \frac{1}{L_{ac}}(v_{dc1} \cdot (S_a - S_b) - r_{ac} \cdot i_{ac} - v_{ac}) \\ \frac{1}{L_{dc}}(2v_{dc2} - v_{dc1} \cdot (S_a + S_b) - r_{dc} \cdot i_{dc2}) \\ \frac{1}{C}(i_{ac} \cdot (S_b - S_a) + i_{dc2}^a \cdot S_a + i_{dc2}^b \cdot S_b) \end{bmatrix} \tag{5}$$

A Finite Control Set Model Predictive Control (FCS-MPC) was chosen to control the converter, given the multi-variable and non-linear characteristic of the system. In order to implement the controller, the model in (5) is converted to its discrete-time equivalent using the forward Euler approximation. Thus, the controller generates the switching states of each leg by performing an minimization of the cost function in (6) with the error of the three controlled variables.

$$J(k+1) = \lambda_1(i_{ac}(k+1) - i_{ac}^*)^2 + \lambda_2(i_{dc2}(k+1) - i_{dc2}^*)^2 + \lambda_3(v_{dc1}(k+1) - v_{dc1}^*)^2 \tag{6}$$

The overall control scheme, shown in Fig. 7, includes an outer PI controller and a reference generator besides the FCS-MPC. The PI controller address the capacitor voltage variations due to the inherent losses of the components of the converter.

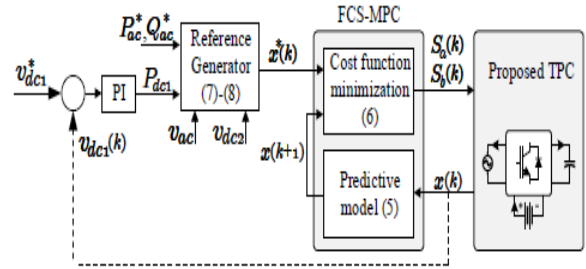


Fig. 7 Control Scheme of FCS-MPC for the proposed TPC.

The output of this outer controller is used, together with the power references at the ac side, to generate the current references for the grid and the battery ports following equations (7)-(8). Then, the MPC controller calculates the optimum signals S_i to drive the converter according to the calculated references. The power balance that led to (8) considers that the power required to keep the voltage of the capacitor (P_{dc1}) will be addressed by the source delivering active power (either the grid or the battery).

$$i_{ac}^* = \frac{P_{ac}^*}{v_{ac} \cdot \cos(\phi)} \quad \text{with} \quad \phi = \arctan\left(\frac{Q_{ac}^*}{P_{ac}^*}\right) \tag{7}$$

$$i_{dc2}^* = \frac{P_{ac}^* + P_{dc1}}{v_{dc2}} \tag{8}$$

A fuzzy control system is based on fuzzy logic, a mathematical system that evaluates analogue input values in terms of continuous logical variables between 0 and 1. Classical or digital logic operates on discrete values of 1 or 0. (true or false, respectively). Machine control uses fuzzy logic. Fuzzy logic deals with things that cannot be represented as "true" or "false" rather as "partially true." Although evolutionary algorithms and neural networks can perform as well as fuzzy logic, fuzzy logic has the benefit that the answer can be expressed in terms that human operators can comprehend, so their experience can be incorporated in the design of the controller. This makes mechanising human-performed jobs easier. In a fuzzy control system, input variables are mapped by "fuzzy sets" of membership functions. "Fuzzification" converts a crisp input to a fuzzy value[16]. A control system may also incorporate switch, or "ON-OFF," inputs along with its analogue inputs. Such switch inputs will always have a truth value of 1 or 0, but the scheme can treat them as

simplified fuzzy functions. The microcontroller decides what action to perform based on "mappings" of input variables into membership functions and truth values. WHEN BRAKES ARE WARM AND SPEED IS SLOW, BRAKE PRESSURE IS LOWERED. "Brake temperature" and "speed" are fuzzy set input variables. "Brake pressure" is a fuzzy output variable that can be "static" or "slightly increased".

IV. FUZZY CONTROL

Fuzzy controls are straightforward. Input, processing, and output. The input stage maps sensors, switches, thumbwheels, etc. to membership functions and truth values. The processing stage invokes each rule, generates a result for each, and combines the results. The output stage translates the combined result into a control output. Most membership functions are triangular, but trapezoidal and bell curves are also employed. The shape is less significant than the quantity and positioning of curves[9][13].

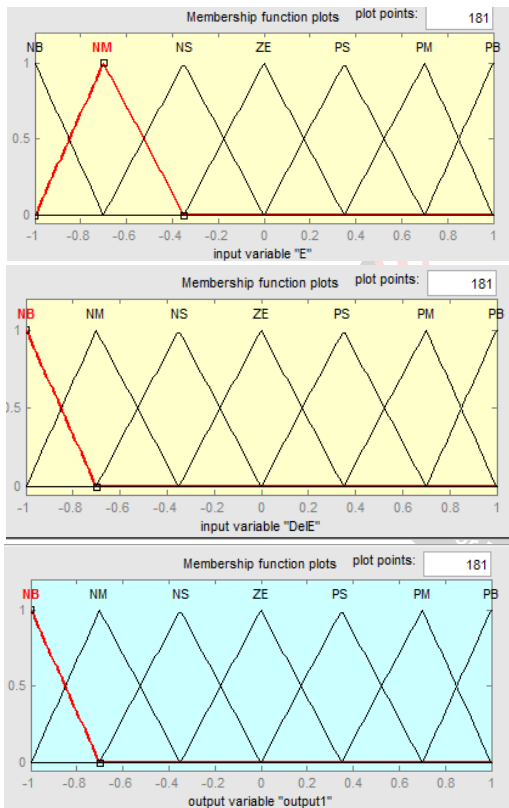


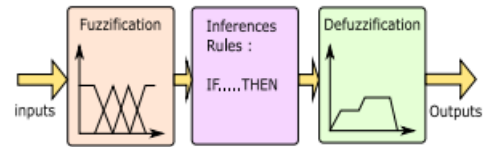
Fig 8 Example figures of input and output membership functions

Fuzzy control system design is based on empirical methods, basically A methodical approach to trial and-error . The general process is as follows:

- Document the system's operational specifications and inputs and outputs.
- Document the fuzzy sets for the inputs.
- Document the rule set.
- Determine the defuzzification method.
- Run through test suite to validate system, adjust details as required.

- Complete document and release to production.

This mechanism is divided into three parts. First, using input membership functions the inputs are fuzzified and then based on rule bases and inference system, outputs are produced and finally the fuzzy outputs are defuzzified and applied to the system. Error and the error change rate are selected as inputs. The block diagram of fuzzy control is represented as follows:



Generally, the design of a fuzzy controller for the control of electric drives requires the choice of the following parameters: linguistic variables, membership functions, inference method , and defuzzification strategy. Fuzzy controller inputs are the error and its derivative, while the output is the command itself. Triangular and trapezoidal membership functions are used on a universe of discourse normalized in the range [-1; 1] for each variable as shown in Fig.8 respectively for the inputs (error, error variation) and output (input process). The subsets fuzzy membership were noted as follows: BN: Big- Negative; SN: Small-Negative; AZ: About-Zero; SP: Small-Positive; BP: Big Positive. The fuzzy rules, for determining output variable of the controller as a function of input variables are grouped in the Table 1.

Table 1 :Inference matrix

		e				
		BN	SN	AZ	SP	BP
Δe	BN	BN	BN	SN	SN	AZ
	SN	BN	SN	SN	AZ	SP
	AZ	BN	SN	AZ	SP	BP
	SP	BN	AZ	SP	SP	BP
	BP	AZ	SP	SP	BP	BP

Fig.9 shows the surface generated by the fuzzy system.

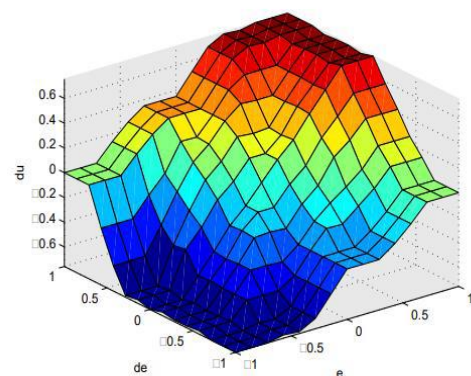


Fig 10 Surface generated by the fuzzy system

The simulation results for the single-phase and three-phase TPCs in Figure 11 are presented in this section. Both configurations described in section II are considered in the single-phase case: a grid-connected BESS that takes advantage of the TPC's boost capabilities; and a hybrid PV-BESS system that provides consistent power to the grid while using the battery as an energy buffer. The three-phase instance, on the other hand, investigates a hybrid system with two dc sources and an ac load.

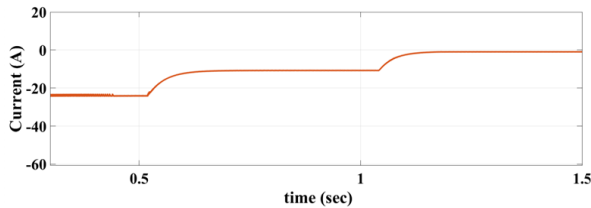


Fig. 11. Simulation results for the single-phase TPC used as a boost inverter for Battery current

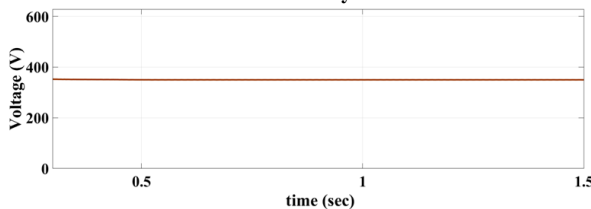


Fig. 12. Simulation results for the single-phase TPC used as a boost inverter for Capacitor voltage

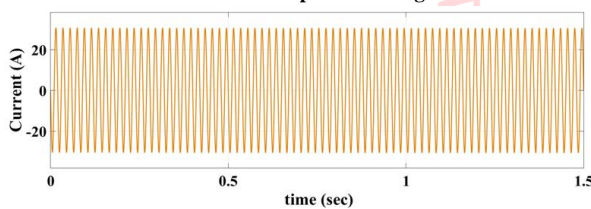


Fig. 13. Simulation results for the single-phase TPC used as a boost inverter for Grid current

In the defined power range, the converter performs correctly; Fig. 11 displays the response of the system's variables as the output power increases from 0.4 to 3 kW. The battery current in Fig. 11(a) is free of low-frequency harmonics and closely tracks the reference with only minor overshoots during step shifts. At the maximum power need, the dc bus voltage is kept at the desired value with a maximum ripple of 10% (Fig. 11(b)). As demonstrated in Fig. 11, the last port, which corresponds to the ac side, has a correct current response with fast tracking of the sinusoidal reference (c). Furthermore, when an active power source such as a PV is attached to the dc1 port, the previously proven regulation of the battery current can be used to supply a steady power output to the ac-grid. This is the second configuration for the single phase scenario, which uses port dc1 as a bidirectional energy buffer and port dc1 as a Unidirectional active power supply. The power response of the system to a change in PV generation[14] is shown in Fig. 12(a), where the battery delivers or absorbs power to address the three-port balance.

Despite differences in the power given at dc1, the bidirectional performance of the battery port is shown in Fig. 12(b), where the regulated current changes correspondingly to keep the ac current constant (Fig. 12(c)).

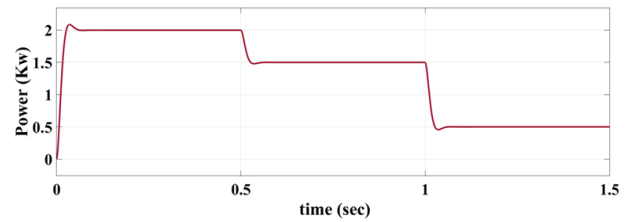


Fig. 14. Simulation results for the single-phase TPC used in a hybrid PVBESS configuration for solar Power at the three ports

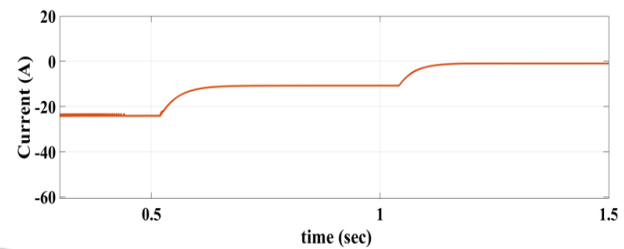


Fig. 15. Simulation results for the single-phase TPC used in a hybrid PVBESS configuration for Current at the battery ports

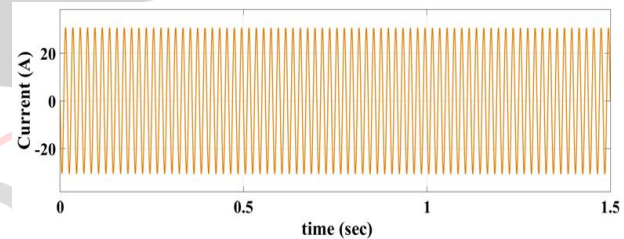


Fig. 16. Simulation results for the single-phase TPC used in a hybrid PVBESS configuration for Grid current

V. CONCLUSION

TPC manages the power of hybrid systems with two dc and one ac port, which lowers topology complexity and increases power density. Each inverter leg functions as a buck boost converter to provide a dc port. Dc and ac ports are separated by inductors. An FLC to lessen V2G data loss and latency is provided in this work. The intelligent controller that is suggested corrects FLC input data based on current node voltage and trend. The grid and MCS conformity of FLC input data is evaluated. stable voltage at the MCS node Data loss and latency due to traditional FLC versus intelligent controller. This novel DC connection eliminates low frequency harmonics, which can hurt batteries or interfere with PV panel MPPT, allowing higher voltage power to be transferred to an ac side. a MIMO non-linear system is modelled to divide power across three ports. In simulations, the FCS-MPC adheres to power configuration references effectively. Simple structures cost less to compute and optimise. Batteries, solar panels, and single-phase and three-phase ac grids can all be used with the converter. The efficiency is on par with typical single-phase grid-connected converters, and research indicates

that it can be increased by adjusting the transistor switching frequency, inductor size, and element voltages.

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